

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Tremblay, Marc; Joy, William
Assignee: Sun Microsystems, Inc.
Title: Local and Global Register Partitioning in a VLIW Processor
Serial No.: 09/204,585 Filing Date: December 3, 1998
Examiner: D. Eng Group Art Unit: 2155
Docket No.: SP-3288 US

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LST
4-11-01
entered

Newport Beach, California
April 3, 2001

BOX CPA
COMMISSIONER FOR PATENTS
Washington, D. C. 20231

PRELIMINARY AMENDMENT/RESPONSE TO FINAL OFFICE ACTION DATED
JANUARY 3, 2001

Dear Sir:

In response to the Final Office Action dated January 3, 2001, please amend this
application as follows:

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IN THE CLAIMS

In accordance with 37 CFR 1.121(c)(1)(ii), Attachment A provides marked up
versions of the claim containing the newly introduced changes.

23. A method of operating a processor comprising:
providing a processor including a plurality of functional units;
a register file divided into a plurality of register file segments, the plurality of register file
segments being



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